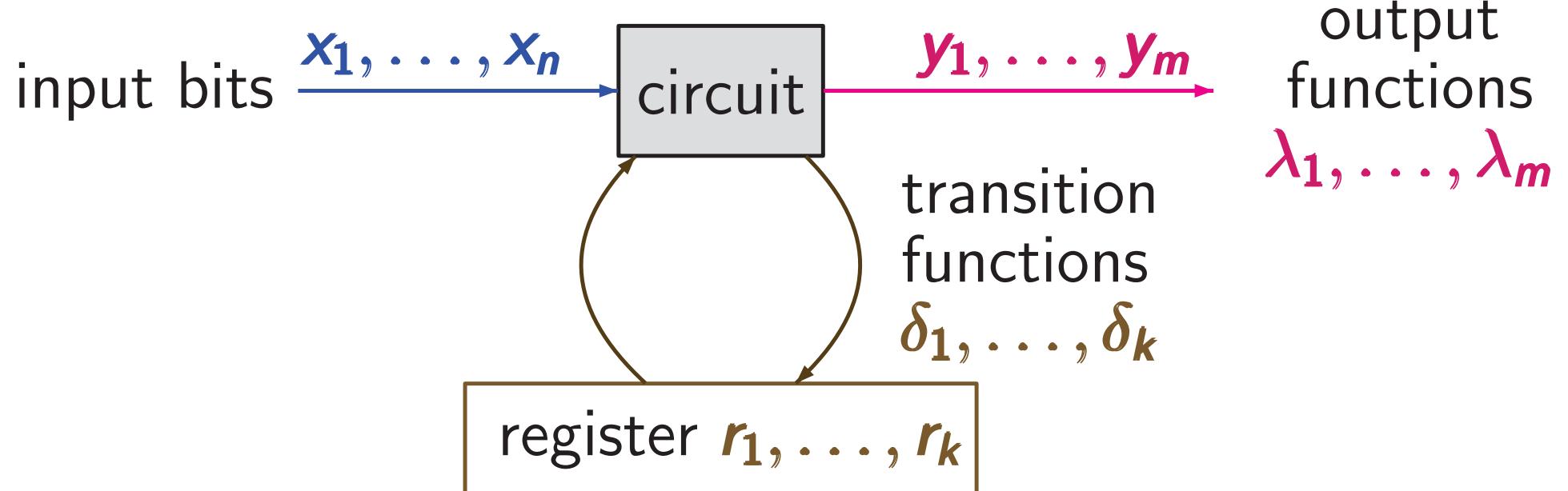


# Modelling of sequential circuits by TS

ts1.4-10



initial register evaluation  $[r_1=c_{01}, \dots, r_k=c_{0k}]$

transition system:

- states: evaluations of  $x_1, \dots, x_n, r_1, \dots, r_k$
- transitions represent the stepwise behavior
- values of input bits change nondeterministically
- atomic propositions:  $x_1, \dots, x_n, y_1, \dots, y_m, r_1, \dots, r_k$